

# 揭秘IEEE IEL数据库： 高效文献调研与学术发表

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**the Institute of Electrical  
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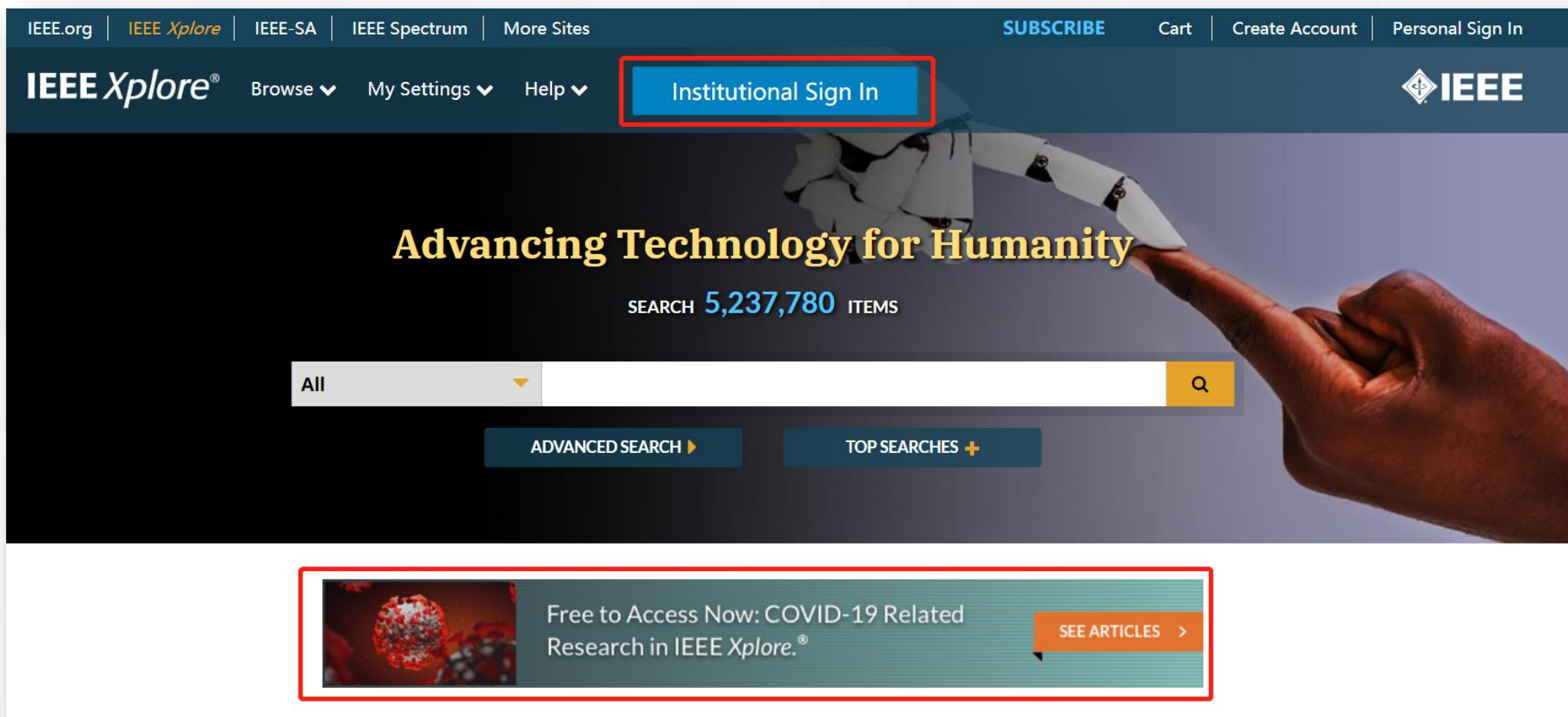
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2020-10-04

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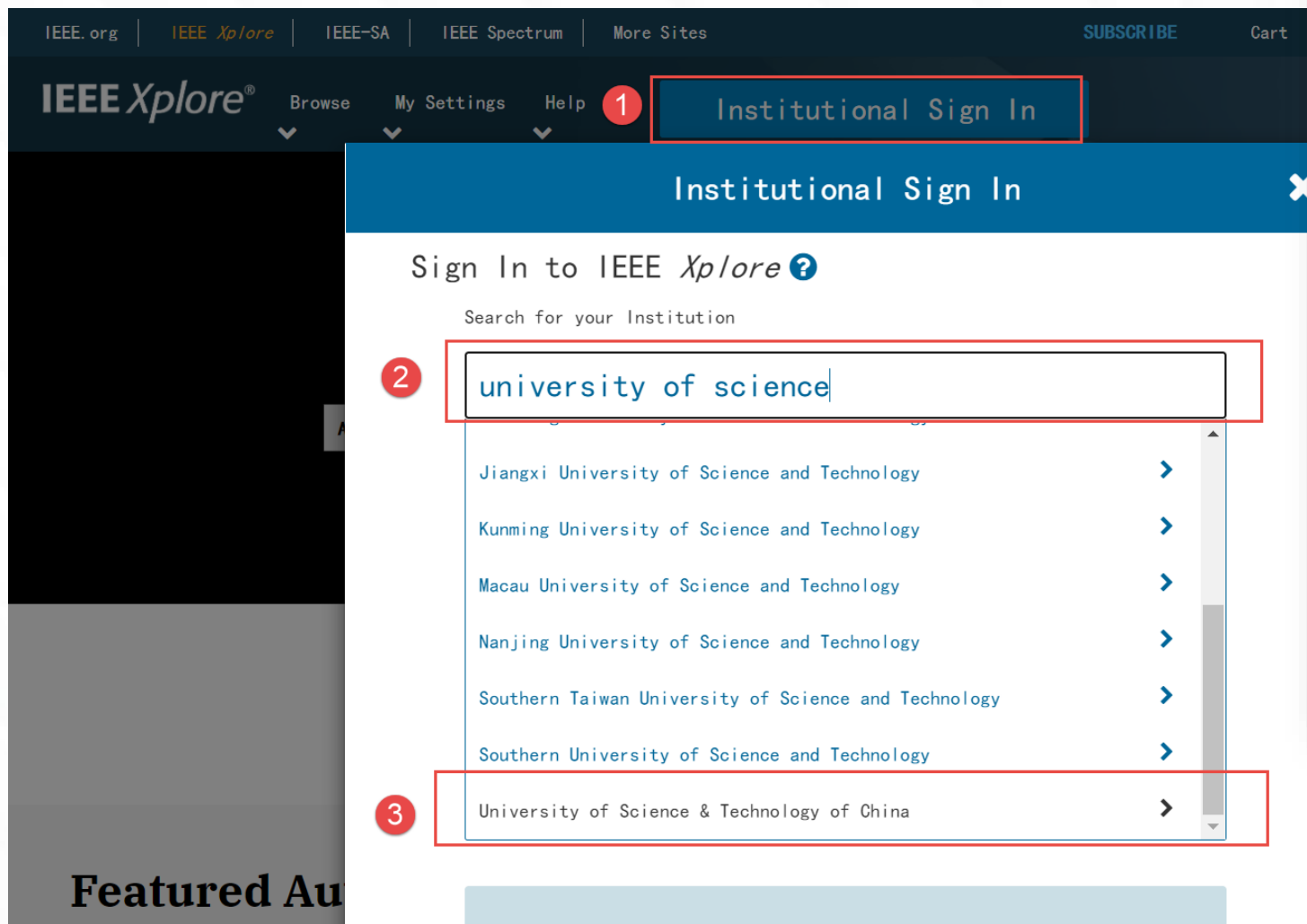
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# 高效文献调研与热点追踪

# 一框式检索核心技术



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2. 检索词之间的默认关系: AND    ie. smart grid= smart AND grid
3. 支持命令检索:    ie. "Abstract":ofdm AND "Publication Title":communications
4. 自动获取词根: pluralized nouns, verb tenses, and British/American spelling variations
5. 精确检索使用双引号: 词组、固定搭配    ie. "java programming"
6. 模糊检索使用\*和?
7. 检索词不区分大小写, 检索运算全部大写

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Affiliation

Publication Title

Enter Title

☐ IEEE Access (814)

☐ IEEE Transactions on Image Processing (455)

☐ IEEE Transactions on Pattern Analysis and Machine Intelligence (383)

☐ 2018 IEEE/CVF Conference on Computer Vision and Pattern Recognition (221)

☐ IEEE Transactions on Multimedia (174)

☐ 2018 24th International Conference on Pattern Recognition (ICPR) (172)

☐ 2017 IEEE Conference on Computer Vision and Pattern Recognition (CVPR) (171)

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A new cloud-based network framework for 5G massive Internet of Things connections

Yongbin Wang ; Zhongping Zhang ; Ping Zhang ; Zheng Ma ; Gang Liu

2017 IEEE 17th International Conference on Communication Technology (ICCT)

Year: 2017 | Conference Paper | Publisher: IEEE

Cited by: Papers (2)

Abstract (html) PDF (910 Kb) CC

Analysis of the Internet of Things devices integration in 5G networks

Lidiia Ignatova ; Abdukodir Khakimov ; Albahri Mahmood ; Ammar Muthanna

2017 Systems of Signal Synchronization, Generating and Processing in Telecommunications (SINKHROINFO)

Year: 2017 | Conference Paper | Publisher: IEEE

Cited by: Papers (3)

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Application of 5G next generation network to Internet of Things

Suresh Borkar ; Himangi Pande

2016 International Conference on Internet of Things and Applications (IOTA)

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Mohammad Javidan Darugar ; Loo Chu Kiong  
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- I. Introduction
- II. IoT Application Requirements
- III. Existing IoT Communication Technology
- IV. 3GPP Cellular Solutions for the Internet of Things
- V. 5G New Radio Enhancements for the Internet of Things

Authors

Figures

References

Citations

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Metrics

Footnotes

## Authors

Shaoqing Ren

University of Science and Technology of China, Hefei, Anhui, China

Shaoqing Ren received the BS degree from the University of Science and Technology of China in 2011. He received the PhD degree in a joint PhD program between University of Science and Technology of China and Microsoft Research Asia. His research interests are in computer vision, especially in detection and localization of general objects and faces.

Kaiming He 

Visual Computing Group, Microsoft Research, Beijing, China

了解作者详情

Kaiming He received the BS degree from Tsinghua University in 2007, and the PhD degree from the Chinese University of Hong Kong in 2011. He is a lead researcher at Microsoft Research Asia. He joined Microsoft Research Asia in 2011. His current research interests are deep learning for visual recognition, including image classification, object detection, and semantic segmentation. He has won the Best Paper Award at CVPR 2009.

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Kaiming He



## Affiliation

Visual Computing Group  
Microsoft Research  
Beijing, China

## Publication Topics

object detection, neural nets, image segmentation, learning (artificial intelligence), feature extraction, image classification, object recognition, computer vision, image annotation, pose

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## Biography

Kaiming He received the BS degree from the Chinese University of Hong Kong. He joined Microsoft Research Asia in 2009, where he works on visual recognition, including image classification. He has won the Best Paper Award at the

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Publication  
Years



## Co-Authors:

Serge Belongie  
Jifeng Dai  
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Piotr Dollár  
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SR Latch: The Wrong Introduction to Digital Memory

Publisher: IEEE Cite This PDF

Abdulhadi Shoufan All Authors

7 Full Text Views

Abstract

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II. Textbook method for introducing the concept of 1-bit memory

III. Technical and pedagogical issues in the textbook method

IV. Alternative Presentation

V. Discussion and Conclusion

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Abstract:

For over 60 years, textbooks on digital logic design have followed a semi-unified way to introduce the concept of digital memory. This method starts from a simple gate-based SR latch and applies several rectifications on it to obtain the D flip-flop. This method seems to have been influenced by the historical evolution of the SR latch from its relay-based variant in the 19th century, through its vacuum-tube version, to the transistor-based implementation. I argue that introducing the digital memory using this approach is problematic for learning as it implies several technical and pedagogical issues. This paper highlights these issues and proposes an alternative method which is based on an intuitive specification of the memory concept and a careful reasoning of related ideas including edge-sensitivity, synchronization, and the clock. I recommend adopting this method in textbooks and other learning resources.

Published in: 2020 IEEE International Symposium on Circuits and Systems (ISCAS)

Date of Conference: 10-21 Oct. 2020 DOI: 10.1109/ISCAS45731.2020.9181122

Date Added to IEEE Xplore: 28 September 2020 Publisher: IEEE

ISBN Information: Conference Location: Sevilla, Spain

ISSN Information:

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2019 IEEE International Conference on Electronics, Computing, and Communication Technologies (CONECT)  
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SR Latch: The Wrong Introduction to Digital Memory

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Jinli Liu, "Base Design" (2023) (arXiv:2304.00001v1 [cs.LG])  
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Abstract: This paper presents a novel framework for deep learning-based image classification. The proposed method leverages a combination of convolutional neural networks (CNNs) and recurrent neural networks (RNNs) to capture both local and global features. The framework is designed to be robust to various types of input data, including noisy and low-resolution images. The results show that the proposed method achieves state-of-the-art performance on several benchmark datasets.

This paper presents a novel framework for deep learning-based image classification. The proposed method leverages a combination of convolutional neural networks (CNNs) and recurrent neural networks (RNNs) to capture both local and global features. The framework is designed to be robust to various types of input data, including noisy and low-resolution images. The results show that the proposed method achieves state-of-the-art performance on several benchmark datasets.


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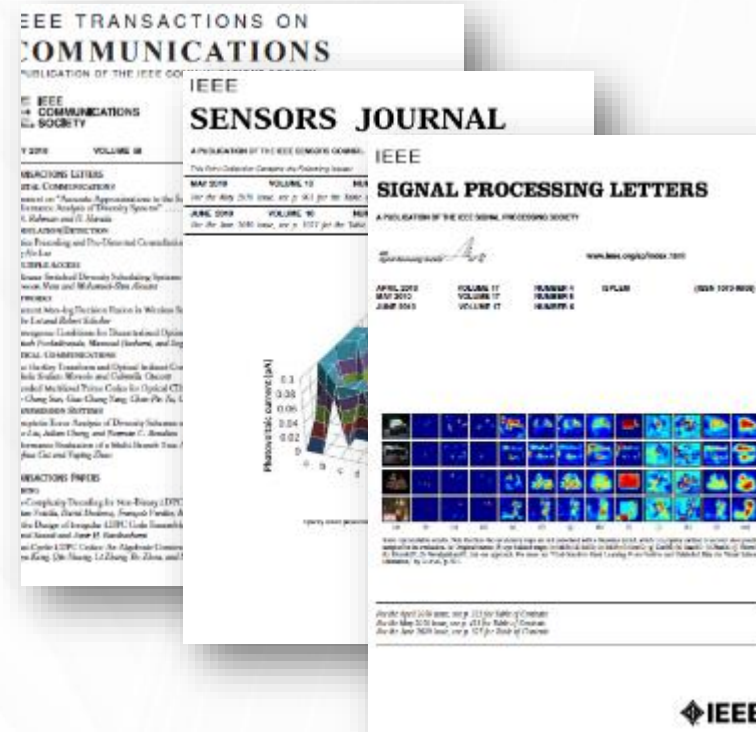
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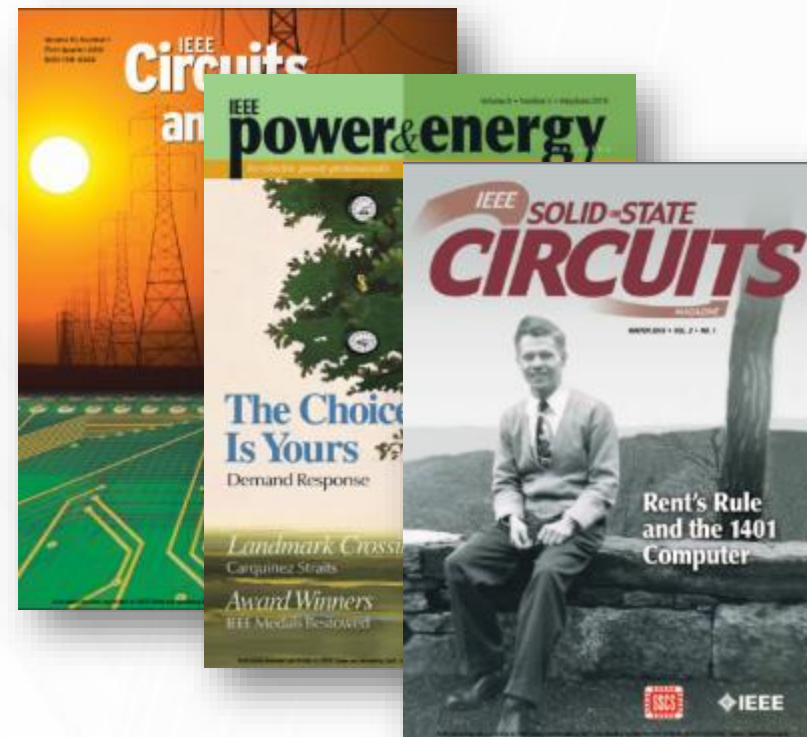
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| <a href="#">2019 IEEE International Conference on Smart Internet of Things (SmartIoT)</a><br>Location: Tianjin University, Tianjin, China  | China     |                              | 09-11 Aug 2019  |
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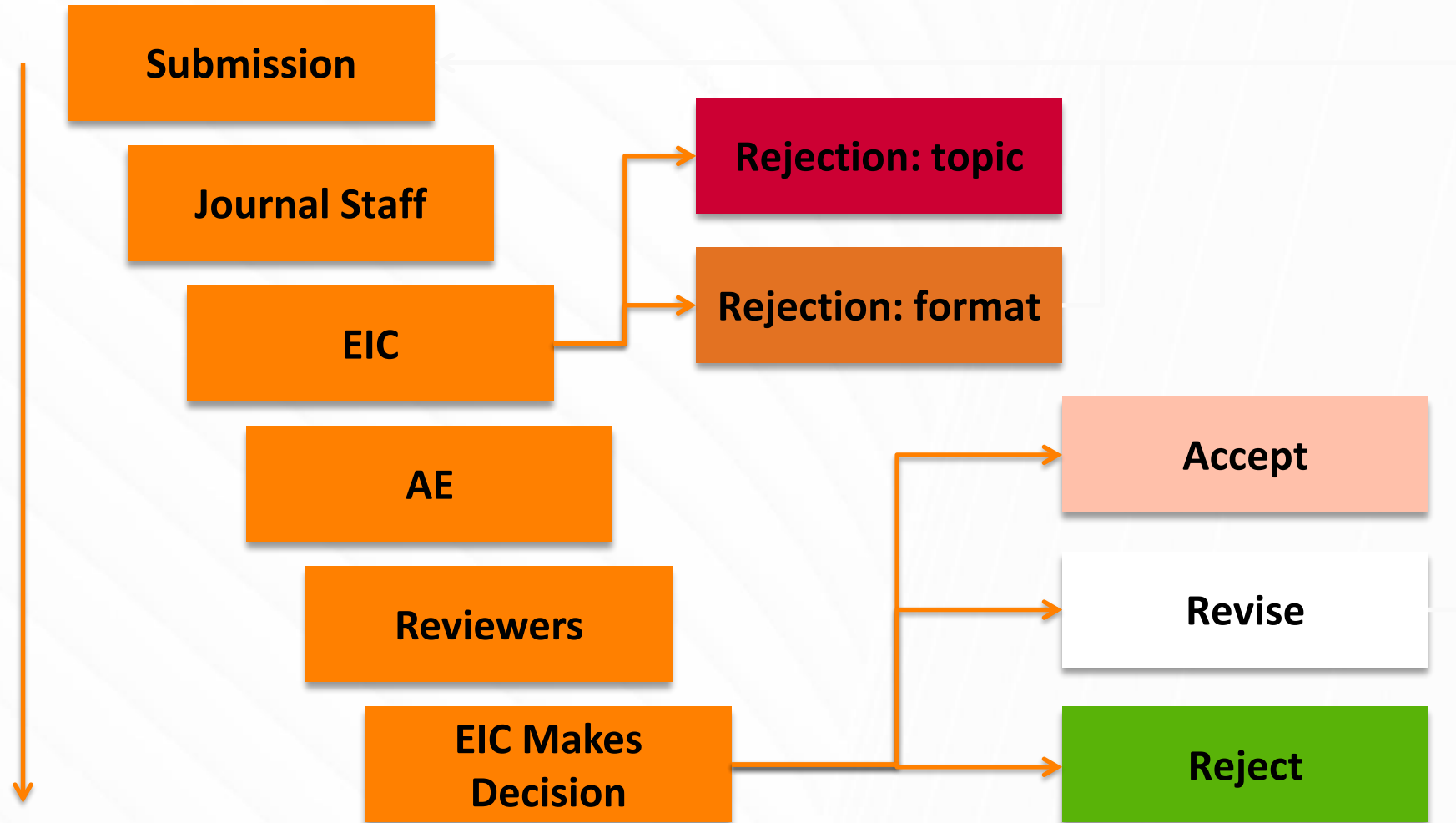
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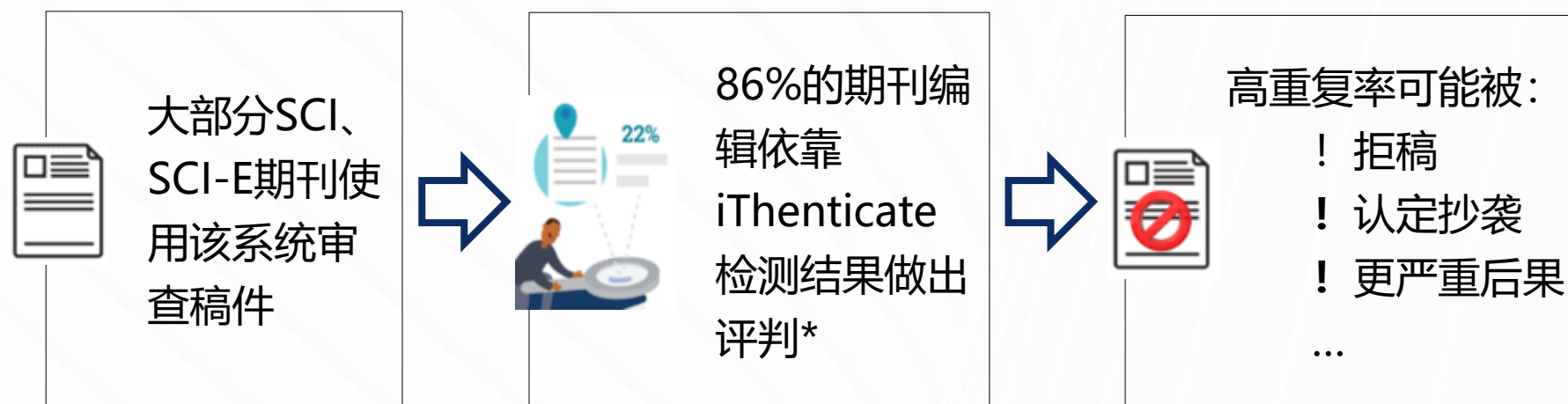
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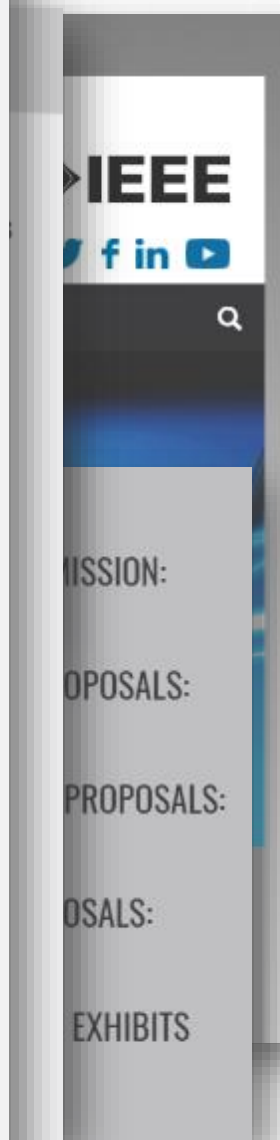
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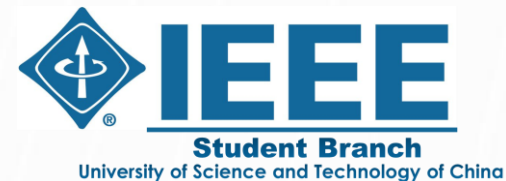
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